

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No.	ALTRP134/ A01466
	Application No.:	10/815,478
	Applicant	BALL, James L.
	Filing Date	March 31, 2004
	Group	2181
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		Page 1 of 1
	Submitted on:	November 6, 2008

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	1.						
	2.						
	3.						
	4.						

Foreign Patent or Published Foreign Patent Application

Examiner Initial		Document No.	Publication Date	Country or Patent Office	Class	Sub-Class	Translation	
							Yes	No
	5.							
	6.							
	7.							
	8.							

Other Documents

Examiner Initial	No.	Author, Title, Place (e.g. Journal) of Publication, Date
	9.	WOLF, Stanley et al. "Silicon Processing For The VLSI Era, Volume 1: Process Technology", 1986, Lattice Press, 2 pages.
	10.	Altera Corporation, Nios II Processor Reference Handbook, Chapter 20 Instruction Set Reference, pages 20-1 thru 20-102, December 2004, San Jose, California
	11.	Final Office Action dated July 30, 2008 in U.S. Application No.: 11/076,307; (ALTRP155)
	12.	
	13.	
	14.	
	15.	

Examiner	Date Considered
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Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.